

No.: 10_2023 /KH-DDT

Ho Chi Minh City, September 20, 2023

PLAN FOR ORGANIZING THE PLC PROGRAMMING CONTEST

Pursuant to Plan No. 786/KH-DCT dated November 9, 2018 of Ho Chi Minh City University of Food Industry regarding the implementation of academic activities for students;

Based on the master plan No. 01/KH-DDT of the Faculty of Electrical – Electronics Engineering on organizing academic activities for students in the academic year 2023–2024;

The Faculty of Electrical – Electronics Engineering hereby develops the plan to organize the PLC Programming Contest as follows:

I. OBJECTIVES AND REQUIREMENTS

1. Objectives

Create an intellectual and creative playground for students of the Faculty of Electrical – Electronics Engineering as well as all students of Ho Chi Minh City University of Industry and Trade.

2. Requirements

- Students enhance their practical skills, consolidate specialized knowledge, and improve teamwork capabilities.
- Promote a strong culture of learning and research throughout the faculty and the university.

II. TIME AND LOCATION

1. Timeframe: from January 18, 2024 to May 30, 2024

- May 16, 2024: opening ceremony and preliminary round
- May 23, 2024: challenge round
- May 30, 2024: final round, closing ceremony, and awarding

2. Location:

Electrical – Electronics Engineering Faculty Practice Workshop,
Address: 31 Che Lan Vien Street, Tay Thanh Ward, Ho Chi Minh City

III. PARTICIPANTS

All students currently studying at Ho Chi Minh City University of Industry and Trade.

IV. REGISTRATION AND REGULATIONS

Each student registers using the form: <https://forms.gle/BUo9X1Ky2XdKWkxY9>

V. SCHEDULE, CONTENT, AND ORGANIZATION

1. Implementation Schedule:

From March 22, 2024 to March 31, 2024: disseminate information to students of all classes.

From March 1, 2024 to April 5, 2024: collect registration lists.

April 6, 2024: finalize the registration list.

May 16, 2024: opening ceremony and preliminary round.

May 23, 2024: challenge round.

May 30, 2024: final round, closing ceremony, and awarding.

2. Organization Details:

Round 1: Preliminary Round – May 16, 2024

- Contestants will randomly draw 1 out of 10 topics provided in advance by the organizers (preliminary round topics will be announced on May 9, 2024).
- The preliminary round consists of I/O control programming using OMRON PLC.
- Results will be evaluated based on the time taken to complete the program.
- Only submissions completed within the allowed time (60 minutes) will be considered.
- The top 20 contestants with the shortest completion times will advance to the Challenge Round.

Round 2: Challenge Round – May 23, 2024

- Contestants will randomly draw 1 out of 5 topics provided in advance (topics will be announced on May 16, 2024; this round also includes system control and monitoring).
- Contestants will randomly draw their workstation positions in the PLC laboratory.
- The challenge round consists of control programming using OMRON PLC.
- Results will be evaluated based on completion time.
- Only submissions completed within the allowed time (90 minutes) will be considered.
- The top 5 contestants with the shortest completion times will advance to the Final Round.

Round 3: Final Round – May 30, 2024

- Contestants will randomly draw 1 out of 5 topics provided in advance (final round topics will be announced on May 23, 2024; this round includes system control, system monitoring, and communication programming between PLCs).
- Contestants will randomly draw their workstation positions in the PLC laboratory.
- The final round consists of control programming using OMRON PLC.
- Only submissions completed within the allowed time (120 minutes) will be considered.
- Results will be evaluated based on completion time.

Before each competition round, the organizing committee will provide guidance for contestants on all exam content. The detailed schedule will be communicated to all registered participants (PLC programming, designing control interfaces, PLC communication, etc.).

3. Violation Handling:

Disqualification and termination of the contestant's test if a short circuit is caused to the system.

Disqualification if contestants communicate with others during the competition.

Disqualification if contestants bring unauthorized materials or commit cheating.

Participants who provoke conflict or exhibit inappropriate behavior that violates school conduct will be removed from the competition.

VI. AWARD STRUCTURE 01 First Prize: 1,200,000 VND

01 Second Prize: 1,000,000 VND

01 Third Prize: 500,000 VND

02 Consolation Prizes: 400,000 VND each

VII. ESTABLISHMENT OF THE ORGANIZING COMMITTEE

1. Mr. Lê Thành Tới, Head of Faculty – Head of Committee
2. Mr. Nguyễn Phú Công, Deputy Head of Automation Department – Member
3. Mr. Trần Văn Hải, Lecturer – Member
4. Ms. Võ Thị Lê Phước, Academic Affairs Officer – Member

The above is the Plan for organizing the PLC Programming Contest of the Faculty of Electrical – Electronics Engineering. All relevant individuals are required to fully and strictly implement the contents of this plan.

Recipients:

- Student Affairs Office & Communication Center
- Filed at the Faculty Office

VICE DEAN OF FACULTY**LÊ THÀNH TỚI**